



COLLEGE OF ENGINEERING
DEPARTMENT OF ELECTRICAL
AND COMPUTER ENGINEERING

DAVIS, CALIFORNIA 95616-5294

Research Positions in Computer Systems and Optical Interconnect Architectures

The Next Generation Networking Systems Laboratory at UC Davis invites applications for Postdoctoral Research Scientists specializing in computing systems and interconnect architecture design.

We are interested in designing new computing systems based on future generation of processors, memories, and interconnects. In particular, we are designing and prototyping massively parallel optical interconnects that will link parallel computing systems with extremely low latency and high throughput. We are seeking candidates with good understanding of computer architectures, benchmarking, workload analysis, and VHDL programming.

Summaries of team projects are available on the web: <http://sierra.ece.ucdavis.edu>. Interested candidates should send their resumes to sbyoo@ucdavis.edu.

Professor S. J. Ben Yoo
University of California
Department of Electrical and Computer Engineering
Davis, California 95616
sbyoo@ucdavis.edu
+1 (530) 752-7063 (office)
+1 (530) 752-8428 (fax)